

FIG. 1

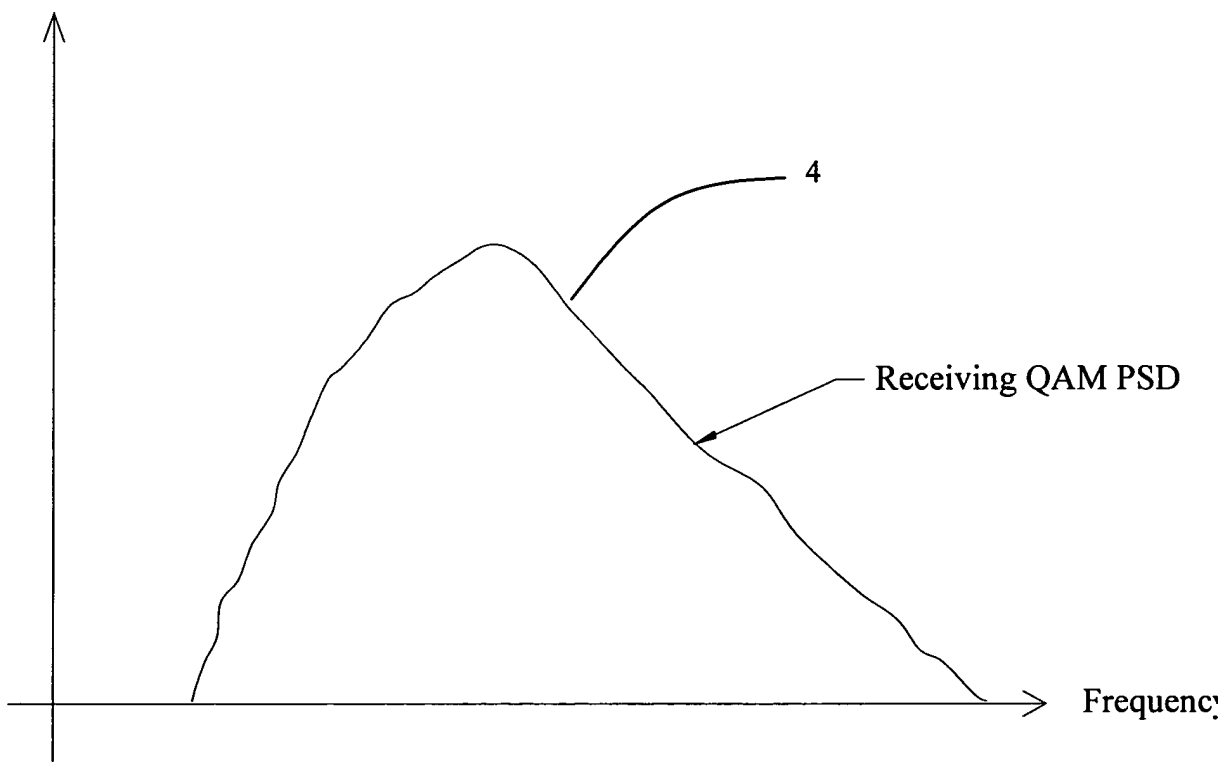


FIG. 2

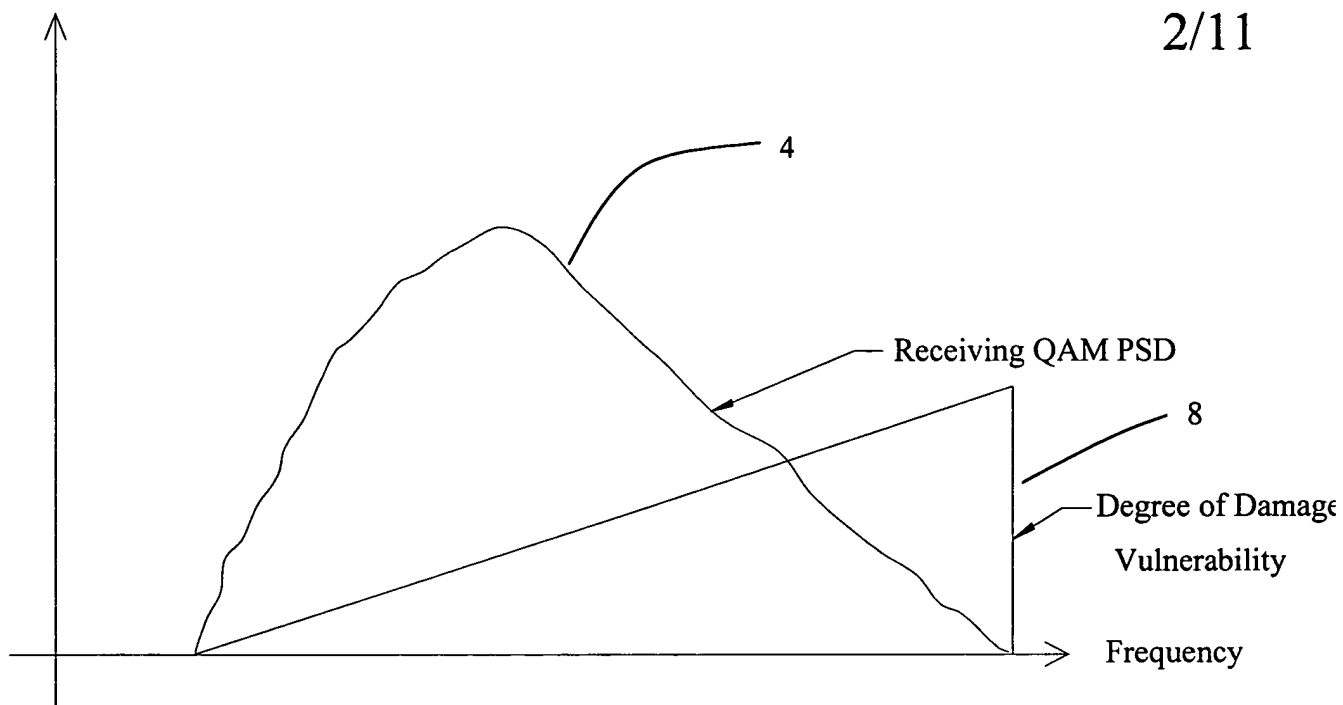


FIG. 3

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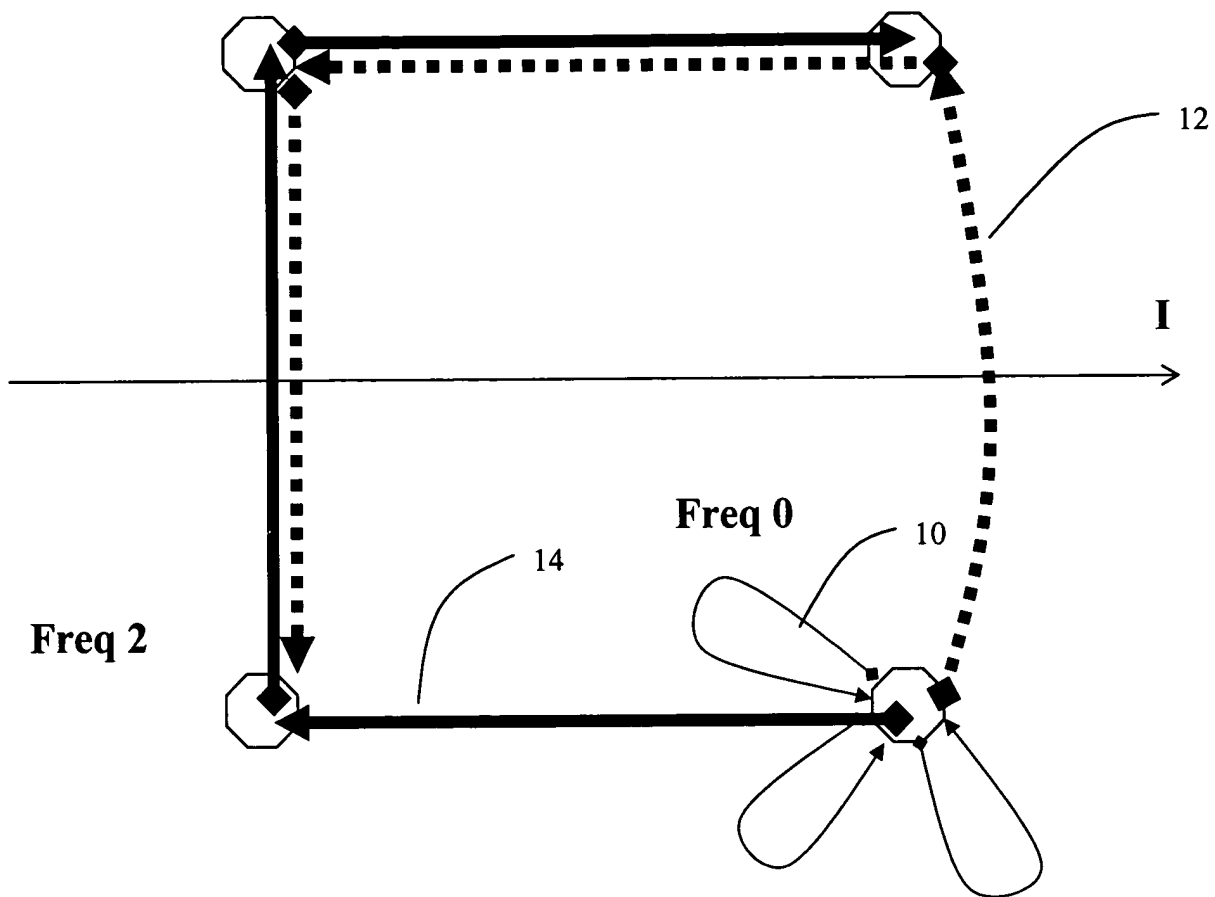


FIG. 4

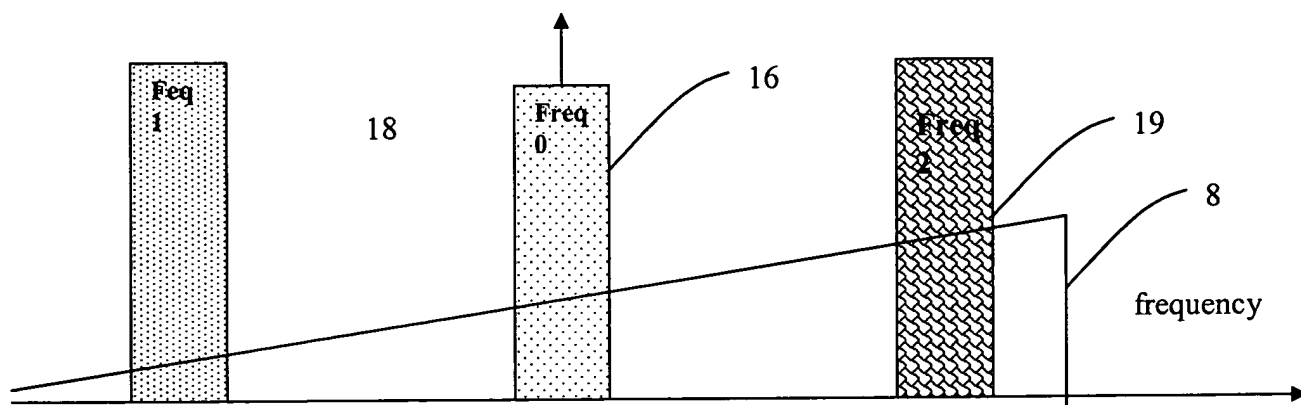


FIG. 5

Last Sample Offset	Current Sample Offset	Next Sample Offset	Relative Damage, i.e. Vulnerability	Notes
<b>0</b>	<b>0</b>	<b>0</b>	<b>3</b>	<b>Freq 0</b>
0	0	$\pi/2$	x	
0	0	$\pi$	x	
0	0	$-\pi/2$	x	
$\pi/2$	0	0	x	
$\pi/2$	0	$\pi/2$	x	
$\pi/2$	0	$\pi$	x	
$\pi/2$	0	$-\pi/2$	x	
$\pi$	0	0	x	
$\pi$	0	$\pi/2$	x	
$\pi$	0	$\pi$	x	
$\pi$	0	$-\pi/2$	x	
$-\pi/2$	0	0	x	
$-\pi/2$	0	$\pi/2$	x	
$-\pi/2$	0	$\pi$	x	
$-\pi/2$	0	$-\pi/2$	x	
$\pi/2$	$\pi/2$	$\pi/2$	<b>7</b>	<b>Freq 2</b>
			x	
$\pi$	$\pi$	$\pi$	<b>4</b>	
			x	
$-\pi/2$	$-\pi/2$	$-\pi/2$	<b>0</b>	<b>Freq 1</b>
			x	

**FIG. 6**

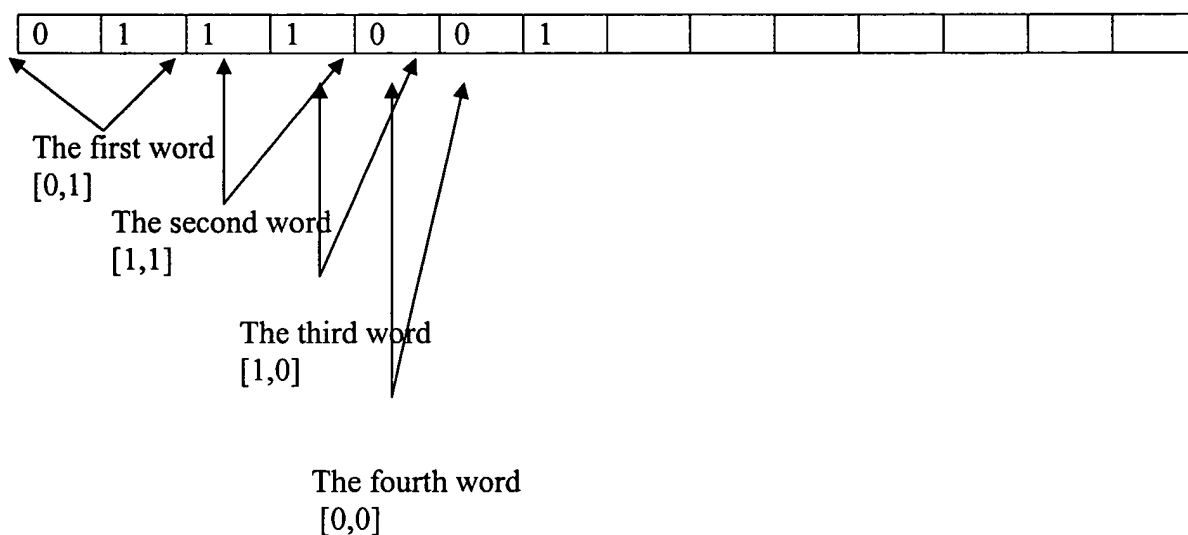
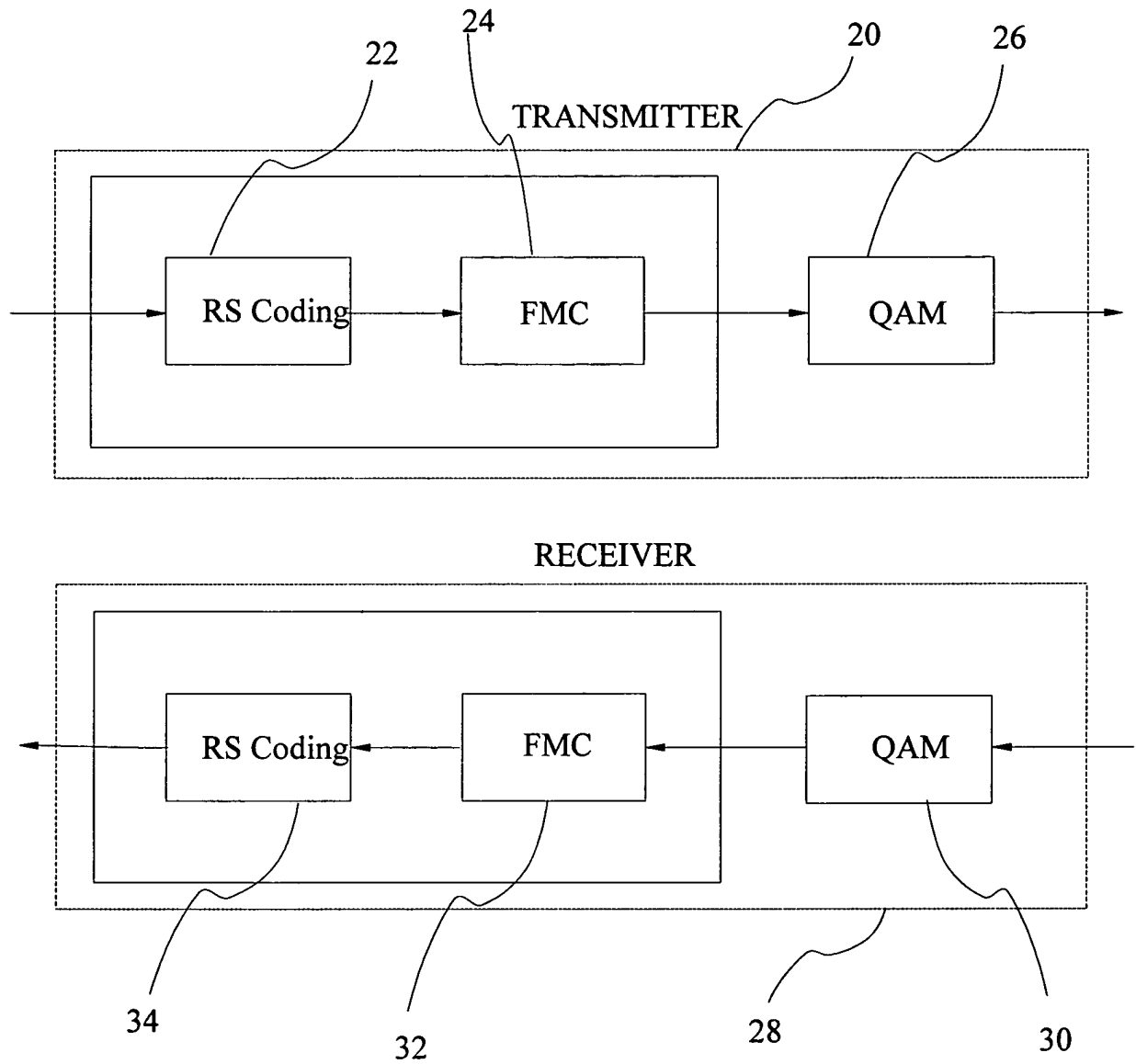


FIG. 7

Input Bit	Mapping bits	Bits should be decoded
00	00	00
01	01	01
10	10	1
11	11	1

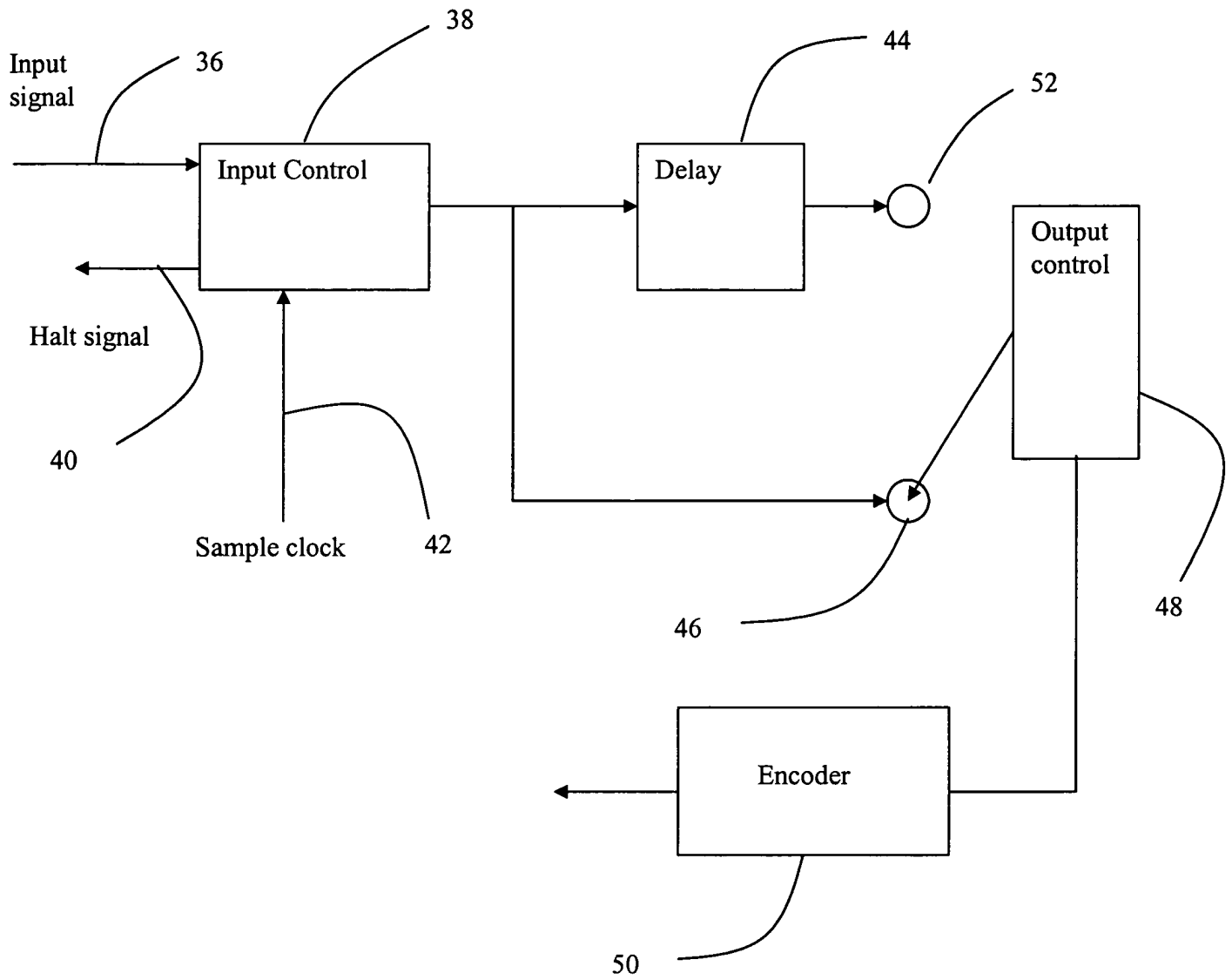
FIG. 8

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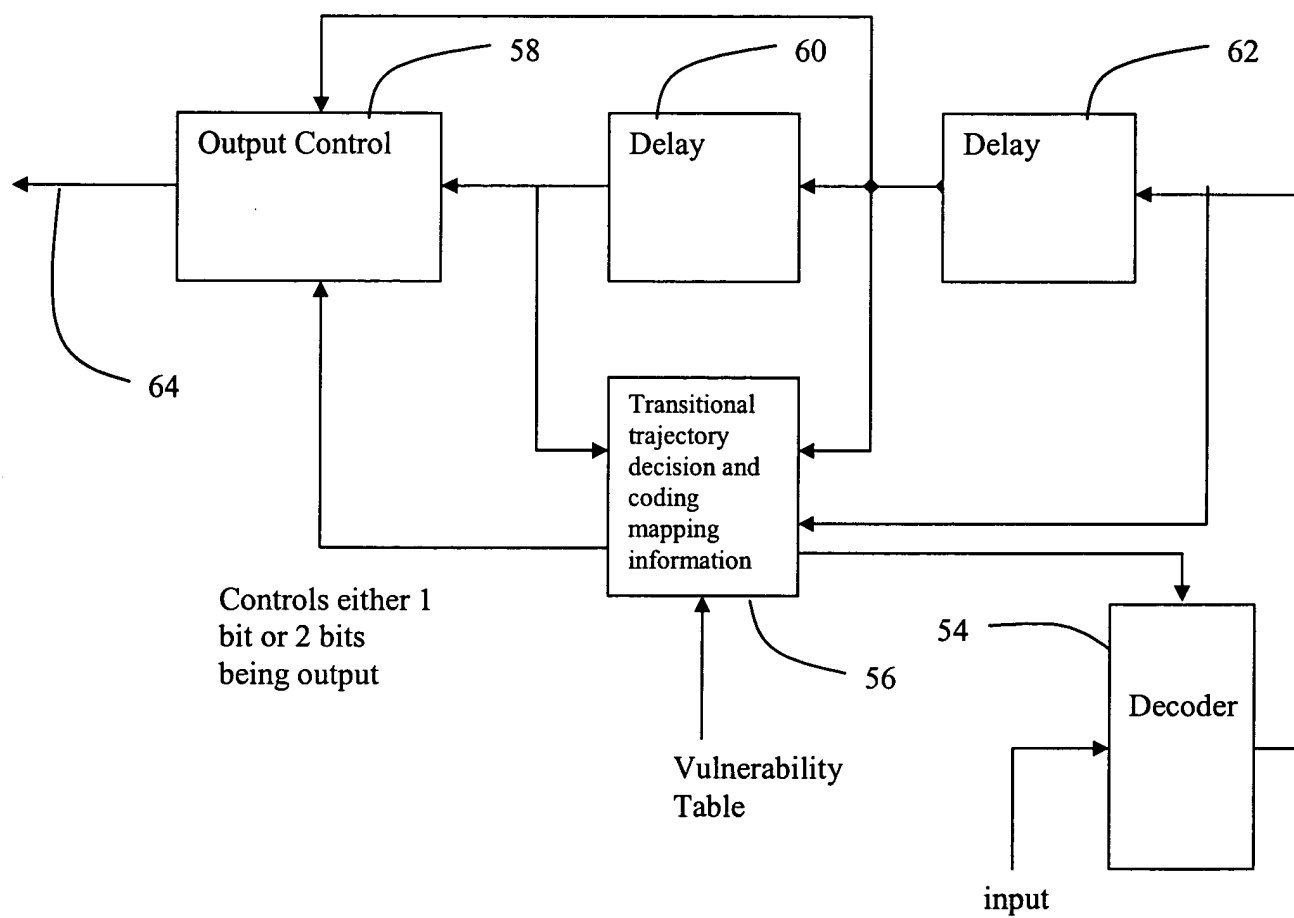
**FIG. 9**

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**FIG. 10**

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**FIG. 11**



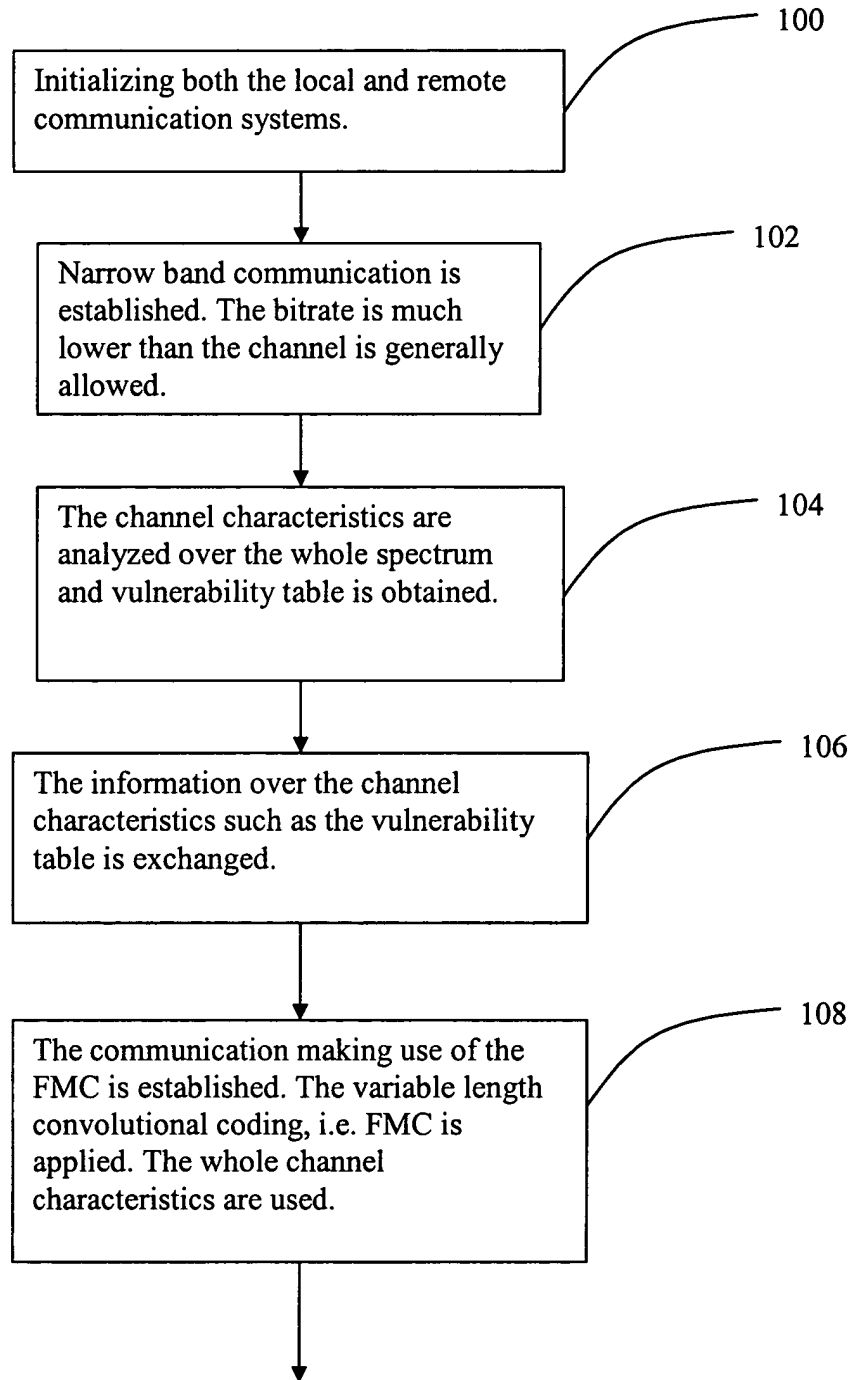


FIG. 12

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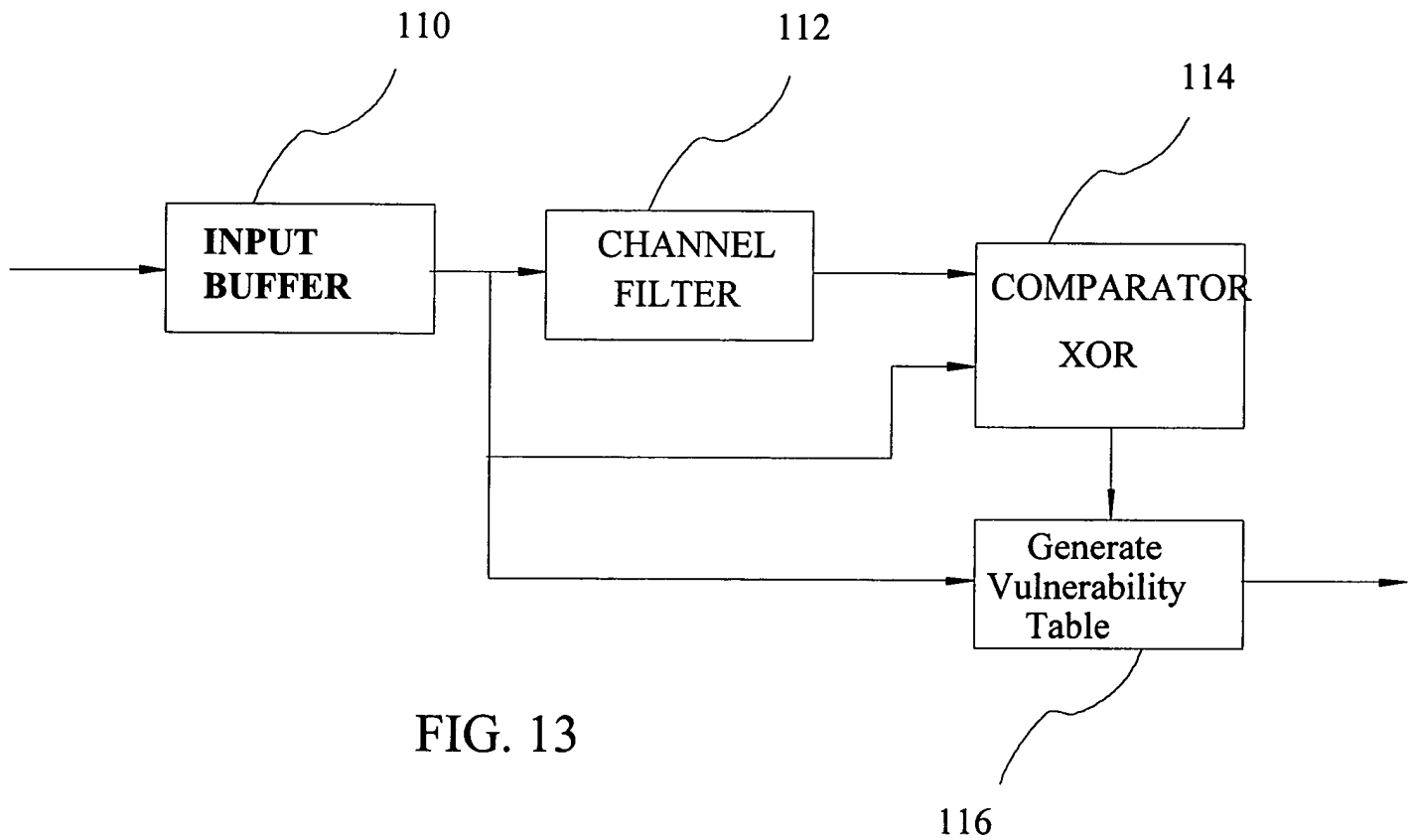
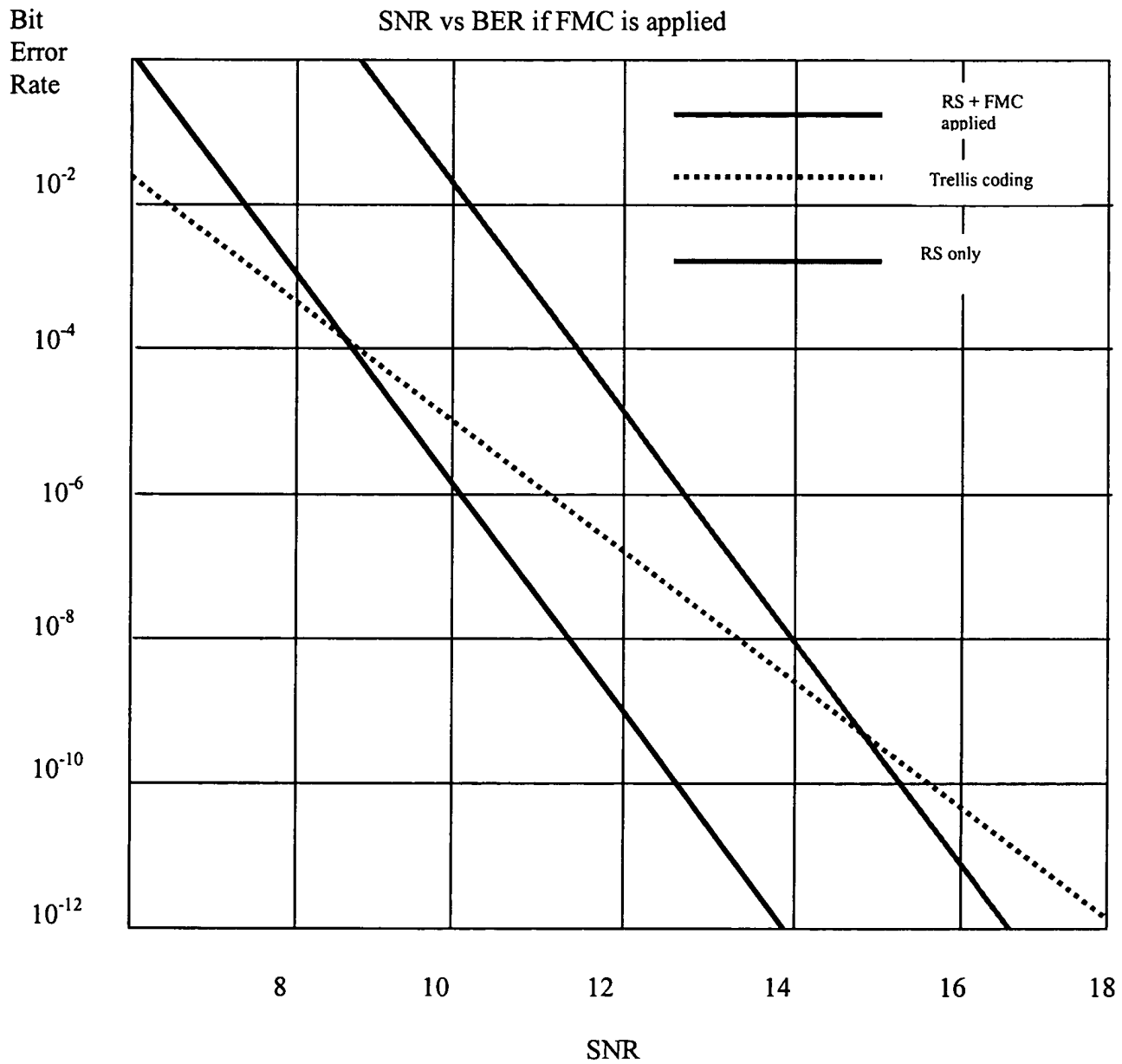


FIG. 13



**FIG. 14**